

LOWER POWER AND REDUCED DEVICE SPLIT  
LOCAL AND CONTINUOUS BITLINE  
FOR DOMINO READ SRAMS

ABSTRACT

The present invention provides for reading indicia from an SRAM cell. A low value is generated on a write true line. A high value is generated on a continuous bit\_line.

5 The true node of the SRAM cell is evaluated through use of a floating voltage coupled to the true node of the SRAM cell. If the floating voltage stays substantially constant, the value read from the SRAM cell is a high. If the floating voltage is drained to ground, the value read from the SRAM

10 cell is a low.